

Figure 1

The diagram illustrates a computer system architecture. A central horizontal bus, labeled 66, connects several components. Above the bus are four components: I/O CONTROLLER (54), SYSTEM MEMORY (52), CENTRAL PROCESSOR (50), and SPEAKER (64). Below the bus are five components: DISPLAY ADAPTER (56), REMOVABLE DISK (58), KEBD (9), FIXED DISK (60), and NETWORK INTERFACE (62). The MONITOR (3) is connected to the DISPLAY ADAPTER (56). Each component is connected to the bus via a bidirectional arrow, indicating data flow in both directions.

Figure 2

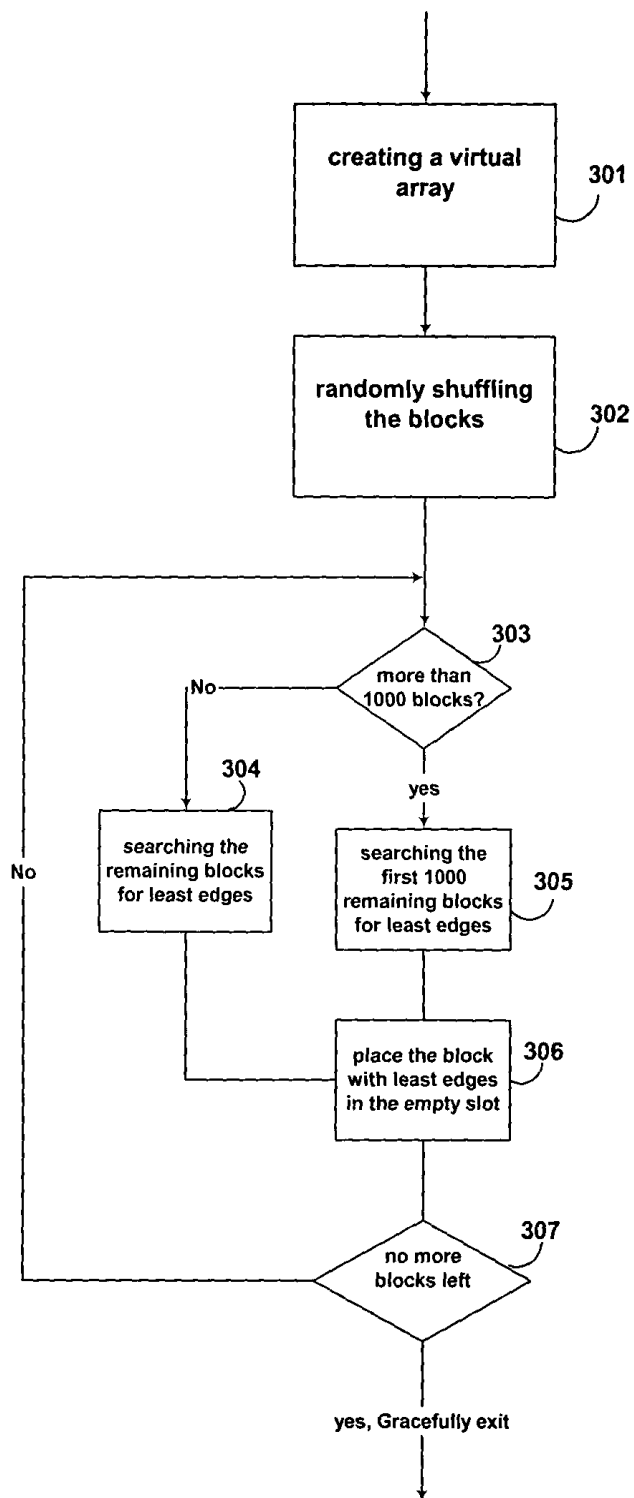


Fig. 3

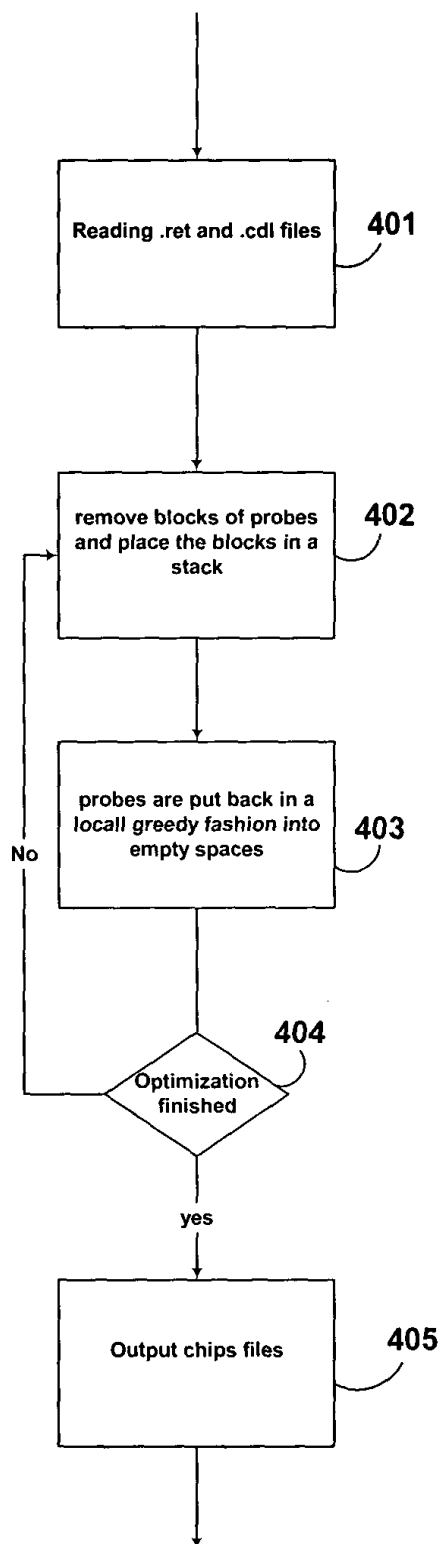


Fig. 4